

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	10744	PLD or (programmable adj logic adj device)	US-PGPU B; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/12/21 17:11
L2	1414	memory adj testing	US-PGPU B; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/12/21 17:11
L3	33089	phase adj locked adj loop\$	US-PGPU B; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/12/21 17:12
L4	17	2 and 3	US-PGPU B; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/12/21 17:12
L5	2	1 and 4	US-PGPU B; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/12/21 17:13
L6	13947	phase adj shifts	US-PGPU B; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/12/21 17:13
L7	11	1 and 2	US-PGPU B; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/12/21 17:14
L8	1	6 and 7	US-PGPU B; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/12/21 17:14
L9	2	3 and 7	US-PGPU B; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/12/21 17:14

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	3	memory adj controller adj emulator	US-PGPU B; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/12/21 12:11
L3	10744	PLD or (programmable adj logic adj device)	US-PGPU B; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/12/21 12:12
L4	13	(error adj correction adj code) with strobe	US-PGPU B; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/12/21 12:21
L5	6181	DDR or (double adj data adj rate)	US-PGPU B; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/12/21 12:15
L6	0	2 same 4	US-PGPU B; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/12/21 12:15
L7	7	2 same 5	US-PGPU B; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/12/21 12:15
L8	400	(711/217).CCLS.	US-PGPU B; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/12/21 12:23
L9	0	3 and 4	US-PGPU B; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/12/21 12:23
L10	11	2 and 3	US-PGPU B; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/12/21 12:23
L11	0	2 and 4	US-PGPU B; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/12/21 12:23

L12	36	2 and 5	US-PGPU B; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/12/21 12:30
L13	96	3 and 5	US-PGPU B; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/12/21 12:24
L14	1	12 and 13	US-PGPU B; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/12/21 12:24
L15	1666	(711/154).CCLS.	US-PGPU B; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/12/21 12:31
L16	652	(714/30).CCLS.	US-PGPU B; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/12/21 12:34
L17	1525	(714/25).CCLS.	US-PGPU B; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/12/21 12:34
L18	549	(714/42).CCLS.	US-PGPU B; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/12/21 12:34